Description: Electronic Circuits

<table>
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<th>Subject</th>
<th>Cat-Nbr</th>
<th>Class</th>
<th>Term</th>
<th>Mode</th>
<th>Units</th>
<th>Campus</th>
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<td>1502</td>
<td>24580</td>
<td>2, 2003</td>
<td>EXT</td>
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Academic Group: FOENS
Academic Org: FOES04
HECS Band: 2
ASCED Code: 031303

STAFFING
Examiner: Mark Norman
Moderator: David Parsons

PRE-REQUISITES
Co-requisite: ELE1801

OTHER-REQUISITES
Co-requisite: 70230

SYNOPSIS
This course introduces the fundamental ideas, theory and devices of electronics. It develops these ideas in an applied way to the extent that the student will be able after successful completion, to use integrated circuits, resistors and capacitors to arrange and build a range of circuits, and to apply basic scientific and mathematical principles to analyse simple circuits. The course is designed around a group of concrete projects which the students respond to, build, test and appropriately document. By this approach, a realistic understanding is gained and attention is focussed on those aspects of electronics as a component of engineering industry which are most valuable.

OBJECTIVES
On completion of this course, students should be able to:

- identify basic scientific principles of simple electronic devices;
- undertake basic d.c. circuit analysis;
- design, assemble and test simple electronic circuits to perform a range of functions. These designs should then be evaluated and appropriately documented;
- analyse simple electronic circuits by applying basic mathematical transfer functions which describe major blocks of the circuit;
- use test equipment to operate and test simple circuits;
- design an assembly of basic functional blocks to perform more complex functions;
- modify given circuits with respect to timing, voltage gain and current rating;
- apply manufacturers' data to a range of simple integrated circuits;
- discuss aspects of the effects of electronics on society.

**TOPICS**

<table>
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<tr>
<td>1. d.c. Circuit Theory</td>
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<tr>
<td>2. Diodes and Transistors - Basics</td>
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<tr>
<td>3. Power Supplies and Linear Voltage Regulators</td>
<td>10.00</td>
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<tr>
<td>4. TTL and CMOS Logic Gates</td>
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<td>5. Monostables and Astables</td>
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<td>6. Comparators</td>
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<td>7. Op amps - Basic Circuits</td>
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<td>8. Power Amplifiers</td>
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<td>9. Electronic Test Equipment</td>
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<td>10. The Effect of Electronics on Society</td>
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**TEXT and MATERIALS required to be PURCHASED or ACCESSED:**

Books can be ordered by fax or telephone. For costs and further details use the 'Book Search' facility at http://bookshop.usq.edu.au by entering the author or title of the text.

An electronic prototyping breadboard - minimum 800 holes.

Various electronic components and tools as per handbook.


**REFERENCE MATERIALS**

Reference materials are materials that, if accessed by students, may improve their knowledge and understanding of the material in the course and enrich their learning experience.

Semiconductor Manufacturers' Data Books

STUDENT WORKLOAD REQUIREMENTS

<table>
<thead>
<tr>
<th>ACTIVITY</th>
<th>HOURS</th>
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<tr>
<td>Directed Study</td>
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<td>Examinations</td>
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<td>Private Study</td>
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<td>Report Writing</td>
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ASSESSMENT DETAILS

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<tr>
<th>Description</th>
<th>Marks Out of</th>
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<th>Due Date</th>
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<tr>
<td>ASSIGNMENT</td>
<td>100.00</td>
<td>10.00</td>
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<td>15 Sep 2003</td>
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<td>PROJECT</td>
<td>200.00</td>
<td>20.00</td>
<td>Y</td>
<td>27 Oct 2003</td>
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<tr>
<td>3 HOUR RESTRICTED EXAMINATION</td>
<td>700.00</td>
<td>70.00</td>
<td>Y</td>
<td>END S2</td>
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NOTES:
- Student Administration will advise students of the dates of their examinations during the semester.

IMPORTANT ASSESSMENT INFORMATION

1 Attendance requirements:
   There are no attendance requirements for this course. However, it is the students' responsibility to study all material provided to them or required to be accessed by them to maximise their chance of meeting the objectives of the course and to be informed of course-related activities and administration.

2 Requirements for students to complete each assessment item satisfactorily:
   To complete each of the assessment items satisfactorily, students must obtain at least 50% of the marks available (or at least a grade of C-) for each assessment item.

3 Penalties for late submission of required work:
   If students submit assignments after the due date without prior approval then a penalty of 20% of the total marks available for the assignment will apply for each working day late.

4 Requirements for student to be awarded a passing grade in the course:
   To be assured of receiving a passing grade a student must attempt all of the summative assessment items, achieve at least 45% in the examination, achieve an aggregated mark of at least 45% in the total marks allocated for the assignments, and at least 50% of the available weighted marks for the summative assessment items.

5 Method used to combine assessment results to attain final grade:
The final grades for students will be assigned on the basis of the weighted aggregate of the marks (or grades) obtained for each of the summative assessment items in the course.

6 Examination information:
In a Restricted Examination, candidates are allowed access to specific materials during the examination. The only materials that candidates may use in the restricted examination for this course are: writing materials (non-electronic and free from material which could give the student an unfair advantage in the examination); calculators which cannot hold textual information (students must indicate on their examination paper the make and model of any calculator(s) they use during the examination).

7 Examination period when Deferred/Supplementary examinations will be held:
Any Deferred or Supplementary examinations for this course will be held during the examination period at the end of the semester of the next offering of this course.

8 University Regulations:
Students should read USQ Regulations 5.1 Definitions, 5.6. Assessment, and 5.10 Academic Misconduct for further information and to avoid actions which might contravene University Regulations. These regulations can be found at the URL http://www.usq.edu.au/SECARIAT/calendar/Part5/ or in the printed version of the current USQ Handbook.

ASSESSMENT NOTES

1 The due date for an assignment is the date by which a student must despatch the assignment to the USQ. The onus is on the student to provide proof of the despatch date, if requested by the Examiner.

2 Students must retain a copy of each item submitted for assessment. This must be despatched to USQ within 24 hours if required by the Examiner.

3 In accordance with University's Assignment Extension Policy (Regulation 5.6.1), the examiner of a course may grant an extension of the due date of an assignment in extenuating circumstances.

4 The Faculty will normally only accept assessments that have been written, typed or printed on paper-based media.

5 The Faculty will NOT accept submission of assignments by facsimile.

6 Students who do not have regular access to postal services or who are otherwise disadvantaged by these regulations may be given special consideration. They should contact the examiner of the course to negotiate such special arrangements.

7 In the event that a due date for an assignment falls on a local public holiday in their area, such as a Show holiday, the due date for the assignment will be the next day. Students are to note on the assignment cover the date of the public holiday for the Examiner's convenience.

8 Students who have undertaken all of the required assessments in a course but who have failed to meet some of the specified objectives of a course within the normally prescribed time may be awarded the temporary grade: IM (Incomplete - Make up). An IM grade will only be awarded when, in the opinion of the examiner, a student
will be able to achieve the remaining objectives of the course after a period of non-directed personal study.

9 Students who, for medical, family/personal, or employment-related reasons, are unable to complete an assignment or to sit for an examination at the scheduled time may apply to defer an assessment in a course. Such a request must be accompanied by appropriate supporting documentation. One of the following temporary grades may be awarded IDS (Incomplete - Deferred Examination; IDM (Incomplete Deferred Make-up); IDB (Incomplete - Both Deferred Examination and Deferred Make-up).