Description: Electronic Workshop and Production

<table>
<thead>
<tr>
<th>Subject</th>
<th>Cat-Nbr</th>
<th>Class</th>
<th>Term</th>
<th>Mode</th>
<th>Units</th>
<th>Campus</th>
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<tr>
<td>ELE</td>
<td>2501</td>
<td>25153</td>
<td>2, 2003</td>
<td>EXT</td>
<td>1.00</td>
<td>TWMBA</td>
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Academic Group: FOENS
Academic Org: FOES04
HECS Band: 2
ASCED Code: 031399

STAFFING
Examiner: Glenn Harris
Moderator: David Parsons

PRE-REQUISITES
Pre-requisite: ELE1801 and ELE1502

OTHER-REQUISITES
Pre-requisite: 70230+70326

SYNOPSIS
A technical officer or professional engineer, employed in the electrical and electronic industry, will at some time be involved in the design and development of electronic equipment. A knowledge of manufacturing processes and construction practice is therefore necessary; whether developing new processes, improving existing processes, using manual skills "hands on" or supervising others using these skills. The information presented in this course will provide the necessary background to manufacturing processes and the manual skills necessary to develop a final product. The aim of this course is to enable the student to comprehend the principles of modern manufacturing processes and to acquire competency in the design, construction and documentation of electronic equipment. The principles of component selection, printed circuit design using CAD software, fabrication and assembly techniques are presented and modern automated assembly using surface mount technology is introduced.

OBJECTIVES
On completion of this course, students should be able to:

- analyse electronic components and interpret their specifications with regard to reliability and selection;
• design a printed circuit board to satisfy client and technical requirements, using computer-assisted techniques;
• construct an item of electronic equipment to a specified standard;
• develop workshop manuals and other documentation for electronic equipment;
• compare modern electronic manufacturing processes and techniques.

TOPICS

<table>
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<tr>
<th>Description</th>
<th>Weighting (%)</th>
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<tbody>
<tr>
<td>1. Component Familiarisation</td>
<td>5.00</td>
</tr>
<tr>
<td>2. Electrical and Electronic Drafting</td>
<td>6.00</td>
</tr>
<tr>
<td>3. PCB Design and Manufacture</td>
<td>14.00</td>
</tr>
<tr>
<td>4. Introduction to Surface Mount Technology</td>
<td>9.00</td>
</tr>
<tr>
<td>5. Workshop Safety and Practice</td>
<td>6.00</td>
</tr>
<tr>
<td>6. Hand Tools</td>
<td>9.00</td>
</tr>
<tr>
<td>7. Mechanical Design and Construction</td>
<td>16.00</td>
</tr>
<tr>
<td>8. Soldering</td>
<td>18.00</td>
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<tr>
<td>9. Introduction to Troubleshooting</td>
<td>6.00</td>
</tr>
<tr>
<td>10. Electronic Equipment Documentation</td>
<td>11.00</td>
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</tbody>
</table>

TEXT and MATERIALS required to be PURCHASED or ACCESSED:

Books can be ordered by fax or telephone. For costs and further details use the 'Book Search' facility at http://bookshop.usq.edu.au by entering the author or title of the text.

Approved Electronics Project.

REFERENCE MATERIALS

Reference materials are materials that, if accessed by students, may improve their knowledge and understanding of the material in the course and enrich their learning experience.


STUDENT WORKLOAD REQUIREMENTS

<table>
<thead>
<tr>
<th>ACTIVITY</th>
<th>HOURS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Assessment</td>
<td>80</td>
</tr>
<tr>
<td>Directed Study</td>
<td>73</td>
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<tr>
<td>Examinations</td>
<td>2</td>
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</table>

ASSESSMENT DETAILS

<table>
<thead>
<tr>
<th>Description</th>
<th>Marks Out of</th>
<th>Wtg(%)</th>
<th>Required</th>
<th>Due Date</th>
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<tbody>
<tr>
<td>PCB DESIGN</td>
<td>150.00</td>
<td>15.00</td>
<td>Y</td>
<td>29 Aug 2003</td>
</tr>
<tr>
<td>ELECTRONIC PROJECT ASSEMBLY</td>
<td>250.00</td>
<td>25.00</td>
<td>Y</td>
<td>24 Oct 2003</td>
</tr>
<tr>
<td>ELECTRONIC PROJECT MANUAL</td>
<td>100.00</td>
<td>10.00</td>
<td>Y</td>
<td>24 Oct 2003</td>
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<tr>
<td>2 HOUR CLOSED EXAMINATION</td>
<td>500.00</td>
<td>50.00</td>
<td>Y</td>
<td>END S2 (see note )</td>
</tr>
</tbody>
</table>

NOTES:
- Student Administration will advise students of the dates of their examinations during the semester.

IMPORTANT ASSESSMENT INFORMATION

1. Attendance requirements:
   There are no attendance requirements for this course. However, it is the students' responsibility to study all material provided to them or required to be accessed by them to maximise their chance of meeting the objectives of the course and to be informed of course-related activities and administration.

2. Requirements for students to complete each assessment item satisfactorily:
   To complete each of the assessment items satisfactorily, students must obtain at least 50% of the marks available (or at least a grade of C-) for each assessment item.

3. Penalties for late submission of required work:
   If students submit assignments after the due date without prior approval then a penalty of 10% of the total marks available for the assignment will apply for each working day late.

4. Requirements for student to be awarded a passing grade in the course:
   To be assured of receiving a passing grade a student must submit all of the summative assessment items, achieve at least 50% in the examination and at least 50% of the available weighted marks for the summative assessment items.
Method used to combine assessment results to attain final grade:
The final grades for students will be assigned on the basis of the weighted aggregate of the marks (or grades) obtained for each of the summative assessment items in the course.

Examination information:
In a Closed Examination, candidates are allowed to bring only writing and drawing instruments into the examination.

Examination period when Deferred/Supplementary examinations will be held:
Any Deferred or Supplementary examinations for this course will be held during the examination period at the end of the semester of the next offering of this course.

University Regulations:
Students should read USQ Regulations 5.1 Definitions, 5.6. Assessment, and 5.10 Academic Misconduct for further information and to avoid actions which might contravene University Regulations. These regulations can be found at the URL http://www.usq.edu.au/SECARIAT/calendar/Part5/ or in the printed version of the current USQ Handbook.

ASSESSMENT NOTES

1. The due date for an assignment is the date by which a student must despatch the assignment to the USQ. The onus is on the student to provide proof of the despatch date, if requested by the Examiner.

2. Students must retain a copy of each item submitted for assessment. This must be despatched to USQ within 24 hours if required by the Examiner.

3. In accordance with University's Assignment Extension Policy (Regulation 5.6.1), the examiner of a course may grant an extension of the due date of an assignment in extenuating circumstances.

4. The Faculty will normally only accept assessments that have been written, typed or printed on paper-based media.

5. The Faculty will NOT accept submission of assignments by facsimile.

6. Students who do not have regular access to postal services or who are otherwise disadvantaged by these regulations may be given special consideration. They should contact the examiner of the course to negotiate such special arrangements.

7. In the event that a due date for an assignment falls on a local public holiday in their area, such as a Show holiday, the due date for the assignment will be the next day. Students are to note on the assignment cover the date of the public holiday for the Examiner's convenience.

8. Students who have undertaken all of the required assessments in a course but who have failed to meet some of the specified objectives of a course within the normally prescribed time may be awarded the temporary grade: IM (Incomplete - Make up). An IM grade will only be awarded when, in the opinion of the examiner, a student will be able to achieve the remaining objectives of the course after a period of non-directed personal study.

9. Students who, for medical, family/personal, or employment-related reasons, are unable to complete an assignment or to sit for an examination at the scheduled time may apply to defer an assessment in a course. Such a request must be accompanied
by appropriate supporting documentation. One of the following temporary grades may be awarded IDS (Incomplete - Deferred Examination; IDM (Incomplete Deferred Make-up); IDB (Incomplete - Both Deferred Examination and Deferred Make-up).

OTHER REQUIREMENTS
1. Students will require access to e-mail and internet access to USQConnect for this course.