Description: Electrical and Electronic Practice B

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<th>Subject</th>
<th>Cat-nbr</th>
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<th>Term</th>
<th>Mode</th>
<th>Units</th>
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<td>1, 2007</td>
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Academic group: FOENS
Academic org: FOES04
Student contribution band: 2
ASCED code: 031300

STAFFING
Examiner: Mark Phythian
Moderator: Alexander Kist

OTHER REQUISITES
Recommended prior or concurrent study: ELE2303 and (ELE2103 or ELE2101)

SYNOPSIS
The purpose of this practice course is to provide experimental support for level two of all internal programs and level three of all external programs in Electrical and Electronic, Computer Systems, Instrumentation and Control Engineering, and Software Engineering. Engineering practice skills, such as the ability to perform practical and project work, innovation, problem identification and solution and engineering judgement will be developed as the students progress through their program. The development of communication skills is also encouraged in the engineering practice courses. This practice course will also reinforce the learning of theory and develop observation and interpretation skills and to stimulate interest and develop self confidence. The course provides equipment familiarisation with control including the examination of first and second order systems as well as microprocessor programming and peripherals and PCB design.

OBJECTIVES
On completion of this course, students should be able to:

1. design an electronic printed circuit board for a specific application using industry standard software;
2. design assembly language programs to configure microprocessor hardware to perform particular tasks;
3. create software to solve a specified problem, while working as part of a team;
4. deduce transfer functions for control system components from measurements;
5. predict open and closed loop control system transfer functions, and transient behaviour, from the characteristics of the system components;
6. analyse the measured performance of a feedback control system in terms of its theoretical model;
7. evaluate the suitability of PID and ON/OFF controllers for various tasks.

**TOPICS**

<table>
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<th>Description</th>
<th>Weighting (%)</th>
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<tr>
<td>Control systems</td>
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<tr>
<td>Microprocessors</td>
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<tr>
<td>PCB design</td>
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**TEXT and MATERIALS required to be PURCHASED or ACCESSED**

ALL textbooks and materials are available for purchase from USQ BOOKSHOP (unless otherwise stated). Orders may be placed via secure internet, free fax 1800642453, phone 07 46312742 (within Australia), or mail. Overseas students should fax +61 7 46311743, or phone +61 7 46312742. For costs, further details, and internet ordering, use the 'Textbook Search' facility at http://bookshop.usq.edu.au click 'Semester', then enter your 'Course Code' (no spaces).

**REFERENCE MATERIALS**

Reference materials are materials that, if accessed by students, may improve their knowledge and understanding of the material in the course and enrich their learning experience.

*ELE2103 Linear Systems and Control External Study Package*, USQ Publication,
*ELE2101 Control and Instrumentation External Study Package*, USQ Publication,
*ELE2501 Electronic Workshop and Production External Study Package*, USQ Publication,
*ELE2303 Embedded Systems Design External Study Package*, USQ Publication,

**STUDENT WORKLOAD REQUIREMENTS**

<table>
<thead>
<tr>
<th>ACTIVITY</th>
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<td>Laboratory or Practical</td>
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<td>Classes</td>
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<td>Report Writing</td>
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**ASSESSMENT DETAILS**

<table>
<thead>
<tr>
<th>Description</th>
<th>Marks out of</th>
<th>Wtg(%)</th>
<th>Due date</th>
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<tbody>
<tr>
<td>REPORTS AND ATTENDANCE</td>
<td>1.00</td>
<td>100.00</td>
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<td>RECORD</td>
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**IMPORTANT ASSESSMENT INFORMATION**

1. Attendance requirements:
It is the students' responsibility to attend and participate appropriately in all activities (such as lectures, tutorials, laboratories and practical work) scheduled for them, and to study all material provided to them or required to be accessed by them to maximise their chance of meeting the objectives of the course and to be informed of course-related activities and administration.

2 Requirements for students to complete each assessment item satisfactorily:
   To satisfactorily complete an assessment item a student must achieve at least 50% of the marks or a grade of at least C-. Students do not have to satisfactorily complete each assessment item to be awarded a passing grade in this course. Refer to Statement 4 below for the requirements to receive a passing grade in this course.

3 Penalties for late submission of required work:
   If students submit assignments after the due date without prior approval then a penalty of 10% of the total marks available for the assignment will apply for each working day late.

4 Requirements for student to be awarded a passing grade in the course:
   To be assured of receiving a passing grade, students must complete at least 80% of the practical and other activities at a satisfactory standard, as stated in the course Assessment Scheme.

5 Method used to combine assessment results to attain final grade:
   As P is the only passing grade available for this course, all students who are qualified for a passing grade, under the requirements in 4 above, will be given a grade of P. Other students will be given either a Failing grade or an Incomplete grade.

6 Examination information:
   There is no examination in this course.

7 Examination period when Deferred/Supplementary examinations will be held:
   Not applicable.

8 University Regulations:
   Students should read USQ Regulations 5.1 Definitions, 5.6. Assessment, and 5.10 Academic Misconduct for further information and to avoid actions which might contravene University Regulations. These regulations can be found at the URL http://www.usq.edu.au/corporateservices/calendar/part5.htm or in the current USQ Handbook.

**ASSESSMENT NOTES**

1 The due date for an assignment is the date by which a student must despatch the assignment to the USQ. The onus is on the student to provide proof of the despatch date, if requested by the Examiner.

2 Students must retain a copy of each item submitted for assessment. This must be produced within five days if required by the Examiner.

3 In accordance with University's Assignment Extension Policy (Regulation 5.6.1), the examiner of a course may grant an extension of the due date of an assignment in extenuating circumstances.

4 The Faculty will normally only accept assessments that have been written, typed or printed on paper-based media.

5 The Faculty will NOT accept submission of assignments by facsimile.

6 Students who do not have regular access to postal services or who are otherwise disadvantaged by these regulations may be given special consideration. They should contact the examiner of the course to negotiate such special arrangements.
7 In the event that a due date for an assignment falls on a local public holiday in their area, such as a Show holiday, the due date for the assignment will be the next day. Students are to note on the assignment cover the date of the public holiday for the Examiner's convenience.

8 Students who have undertaken all of the required assessments in a course but who have failed to meet some of the specified objectives of a course within the normally prescribed time may be awarded the temporary grade: IM (Incomplete - Make up). An IM grade will only be awarded when, in the opinion of the examiner, a student will be able to achieve the remaining objectives of the course after a period of non-directed personal study.

9 Students who, for medical, family/personal, or employment-related reasons, are unable to complete an assignment or to sit for an examination at the scheduled time may apply to defer an assessment in a course. Such a request must be accompanied by appropriate supporting documentation. One of the following temporary grades may be awarded IDS (Incomplete - Deferred Examination; IDM (Incomplete Deferred Make-up); IDB (Incomplete - Both Deferred Examination and Deferred Make-up)).